

## United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
09/893,676	9/893,676 06/29/2001		Hyeon Ho Son	49128-5018	9570		
9629	7590	03/22/2005		EXAM	EXAMINER		
		& BOCKIUS LLP IA AVENUE NW	NGUYEN, JENNIFER T				
WASHINGTON, DC 20004				ART UNIT	PAPER NUMBER		

DATE MAILED: 03/22/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		A lia salia Na	Annticont(a)				
		Application No.	Applicant(s)				
	055	09/893,676	SON ET AL.				
	Office Action Summary	Examiner	Art Unit				
		Jennifer T Nguyen	2674				
Period fo	The MAILING DATE of this communication	on appears on the cover sheet wi	th the correspondence address	; <b></b>			
A SH THE - Exte after - If the - If NO - Failu Any	ORTENED STATUTORY PERIOD FOR F MAILING DATE OF THIS COMMUNICAT nsions of time may be available under the provisions of 37 (SIX (6) MONTHS from the mailing date of this communicat e period for reply specified above is less than thirty (30) days period for reply is specified above, the maximum statutory ire to reply within the set or extended period for reply will, by reply received by the Office later than three months after the ed patent term adjustment. See 37 CFR 1.704(b).	CION.  CFR 1.136(a). In no event, however, may a region.  s, a reply within the statutory minimum of thirt period will apply and will expire SIX (6) MON a statute, cause the application to become AB	eply be timely filed y (30) days will be considered timely. THS from the mailing date of this communi ANDONED (35 U.S.C. § 133).	ication.			
Status							
1)🖂	Responsive to communication(s) filed on	21 January 2005.					
		This action is non-final.					
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
5)□ 6)⊠ 7)⊠	Claim(s) 1-24 is/are pending in the applic 4a) Of the above claim(s) is/are wi Claim(s) is/are allowed.  Claim(s) 1-4,7-17 and 20-24 is/are reject Claim(s) 5,6,18 and 19 is/are objected to Claim(s) are subject to restriction is	thdrawn from consideration. ed.					
Applicat	ion Papers						
9)[	The specification is objected to by the Exa	aminer.					
10)	D) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
11)[	Replacement drawing sheet(s) including the of the oath or declaration is objected to by the oath or declaration is objected to by the oath or declaration is objected to be the oath or declaration is objected to be ob	,	• •	` '			
Priority ι	ınder 35 U.S.C. § 119						
a)	Acknowledgment is made of a claim for for All b) Some * c) None of:  1. Certified copies of the priority docu 2. Certified copies of the priority docu 3. Copies of the certified copies of the application from the International Elee the attached detailed Office action for	iments have been received. iments have been received in A e priority documents have been Bureau (PCT Rule 17.2(a)).	pplication No received in this National Stage	e			
Attachmen	· ·						
	te of References Cited (PTO-892)		ummary (PTO-413) )/Mail Date				
3) 🔲 Infor	e of Draftsperson's Patent Drawing Review (PTO-94 mation Disclosure Statement(s) (PTO-1449 or PTO/5 or No(s)/Mail Date		oformal Patent Application (PTO-152)				

Application/Control Number: 09/893,676 Page 2

Art Unit: 2674

## **DETAILED ACTION**

1. This Office action is responsive to amendment filed on 01/21/2005.

## Claim Rejections - 35 USC § 103

- 2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 3. Claims 1-4, 7-17, and 20-24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Zavracky et al. (Patent No.: US 6,552,704) in view of Sugawara et al. (Patent No.: US 6,504,523).

Regarding claims 1 and 13, referring to Figs. 12A-12C, Zavracky teaches a method of driving a liquid crystal display device (1112) during one display frame, comprising the steps of: applying one of a high level common voltage (Vcom high) and a low level common voltage (Vcom low) to a plurality of liquid crystal cells of the liquid crystal display device (1112) to write data into the liquid crystal cells within a time interval shorter than one display frame interval; and turning on a backlight (1111) after said data writing to display an image (col. 10, lines 37-67, col. 11, lines 1-46, and col. 13, lines 23-49).

Zavracky differs from claims 1 and 13 in that he does not specifically teaches applying a reference common voltage to the plurality of liquid crystal cells after applying the one of the high level common voltage and low level common voltage. However, referring to Figs. 3 and 10, Sugawara teaches applying a reference common voltage (Vcom = 5V) to the plurality of liquid

Application/Control Number: 09/893,676

Art Unit: 2674

crystal cells after applying the one of the high-level common voltage (Vgon = 19V) and low-level common voltage (Vgoff = -10V) (col. 2, lines 43-58 and col. 6, line 66 to col. 7, line 20). Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the reference common voltage as taught by Sugawara in the system of Zavracky in order to prevent the flicker phenomenon and improve the picture quality.

Regarding claims 2 and 15, referring to Fig. 12B, Zavracky further teaches after applying one of the high-level common voltage and the low-level common voltage, the liquid crystal cells to respond according to the data written between the time when the data is written and when the backlight (1111) is turned on (col. 11, lines 1-35).

Regarding claims 3 and 14, the combination of Zavracky and Sugawara teaches the reference common voltage lower than the high-level common voltage and greater than the low-level common voltage (col. 2, lines 43-58 and col. 6, line 66 to col. 7, line 20 of Sugawara).

Regarding claims 4 and 17, referring to Fig. 12B, Zavracky teaches re-aligning the liquid crystal cells after the step of turning on the backlight (col. 13, lines 23-49).

Regarding claim 7, Zavracky teaches when data is being written, an effective voltage remaining in the liquid crystal cell is larger than a data voltage applied to the liquid crystal cell (col. 11, line 64 to col. 12, lines 8).

Regarding claims 12 and 24, Zavracky teaches the driving method is applied to twisted nematic mode liquid crystal display device (col. 13, lines 1-2).

Regarding claims 8, 10, 20, and 22, the combination of Zavracky and Sugawara teaches the high-level common voltage is +19V and the low level common voltage is -10V (Fig. 3 of Sugawara).

Regarding claims 9 and 21, Zavracky further teaches the high-level common voltage is equal to a gate high voltage applied to a gate electrode of a thin film in transistor of the liquid crystal cell (Figs. 12A and 12B, col. 10, lines 55-67, col. 11, lines 1-22).

Regarding claims 11 and 23, Zavracky further teaches the low-level common voltage is equal to a gate low voltage applied to a gate electrode of a thin film transistor in the liquid crystal cell (Figs. 12A and 12B, col. 10, lines 55-67, col. 11, lines 1-22).

Regarding claim 16, Zavracky further teaches one of the high level and low level common voltages (i.e., VcomLow) is applied to the liquid crystal cells after the step of turning on (Fig. 12B).

- 4. Claims 5, 6, 18, and 19 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.
- 5. Applicant's arguments with respect to claims 1-24 have been considered but are moot in view of the new ground(s) of rejection.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer T Nguyen whose telephone number is 571-272-7696. The examiner can normally be reached on Mon-Fri: 9:00am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Patrick N. Edouard can be reached on 571-272-7603. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Application/Control Number: 09/893,676 Page 5

Art Unit: 2674

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JNguyen 03/15/05

AEGINA LIANG
PRIMARY EXAMINER